

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L6	4176	(flash near5 memory).clm.	US-PGPU B	OR	ON	2005/10/29 18:20
L7	125824	(substrate wafer).clm.	US-PGPU B	OR	ON	2005/10/29 18:21
L8	231448	(trench recess groove open\$3 hole mesa).clm.	US-PGPU B	OR	ON	2005/10/29 18:21
L9	31	((polysilicon conductive) same line same stud).clm.	US-PGPU B	OR	ON	2005/10/29 18:22
L10	12355	(lower same portion same (trench recess groove open\$3 hole mesa)).clm.	US-PGPU B	OR	ON	2005/10/29 18:22
L11	12806	(source same line).clm.	US-PGPU B	OR	ON	2005/10/29 18:22
L12	275	(upper same portion same conductive same line).clm.	US-PGPU B	OR	ON	2005/10/29 18:23
L13	2217	((insulating dielectric) same ((polysilicon conductive) same line)).clm.	US-PGPU B	OR	ON	2005/10/29 18:23
L14	180	(( polysilicon conductive) same spacer same ((upper top) same sidewall same (trench recess groove hole open\$3 mesa))).clm.	US-PGPU B	OR	ON	2005/10/29 18:24
L15	7	(floating same gate).clm. and ((insulat\$3 dielectric) same stud).clm.	US-PGPU B	OR	ON	2005/10/29 18:25
L16	6106	(control near5 gate).clm.	US-PGPU B	OR	ON	2005/10/29 18:25
L17	0	6 and 7 and 8 and 9 and 10 and 11 and 12 and 13 and 14 and 15 and 16	US-PGPU B	OR	ON	2005/10/29 18:26
L18	2	15 and 14	US-PGPU B	OR	ON	2005/10/29 18:27

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	23042	float\$3 near5 gate	US-PGPU B; USPAT; USOCR	OR	ON	2005/10/29 17:45
L2	134436	control near5 gate	US-PGPU B; USPAT; USOCR	OR	ON	2005/10/29 16:54
L3	16335	1 and 2	US-PGPU B; USPAT; USOCR	OR	ON	2005/10/29 16:54
L4	12118	3 and (trench recess groove hole open\$3 mesa contact)	US-PGPU B; USPAT; USOCR	OR	ON	2005/10/29 17:46
L5	3199	4 and spacer	US-PGPU B; USPAT; USOCR	OR	ON	2005/10/29 17:46
L6	2729	5 and (insulat\$3 liner)	US-PGPU B; USPAT; USOCR	OR	ON	2005/10/29 17:47
L7	2684	6 and (source drain)	US-PGPU B; USPAT; USOCR	OR	ON	2005/10/29 16:56
L8	2641	6 and (source and drain)	US-PGPU B; USPAT; USOCR	OR	ON	2005/10/29 16:56
L9	1406	8 and (silicide salicide)	US-PGPU B; USPAT; USOCR	OR	ON	2005/10/29 16:57
L10	19488	float\$3 and gate	EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/10/29 17:46
L11	142275	control and gate	EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/10/29 17:46
L12	10345	10 and 11	EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/10/29 17:46

L13	2468	12 and (trench recess groove hole open\$3 mesa contact)	EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/10/29 17:46
L16	287	13 and spacer	EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/10/29 17:47
L17	132	16 and (insulat\$3 liner)	EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/10/29 17:47